
	VIVEKANANDHA COLLEGE OF ENGINEERING FOR WOMEN (AUTONOMOUS)	
OFFICE OF THE CONTROLLER OF THE EXAMINATIONS		

**TIME TABLE - B.E. / B.Tech. (R-2019) DEGREE END-SEMESTER (ARREAR) EXAMINATIONS – JUNE / JULY 2024
for Candidates admitted in 2021**

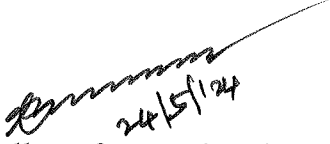
FN: 10.00 AM – 1.00 PM

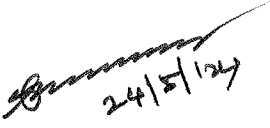
Date		25.06.2024	
Degree /Branch	Sem	FN	
B.E. BME	V	U19EC411 – Digital Signal Processing*	

Note:

* Third year B.E. (BME) students should write their V Semester U19EC411 – Digital Signal Processing arrear exam along with IV semester B.E. (ECE) students.


 DCoE


 24/5/24
 Controller of Examinations


 24/5/24
 AP Principal